Response to Office Action Dated 07/25/2005

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REMARKS

In view of the following remarks, Applicant respectfully requests reconsideration and allowance of the subject application. This amendment is believed to be fully responsive to all issues raised in the 07/25/2005 Office Action.

In the Claims:

No claims are added.

Claims 3, 6, 8, 12, 16, 18 and 19 are original.

Claims 7 and 15 are currently amended.

Claims 1, 4, 5, 11, 17 were previously presented.

Accordingly, claims 1 and 3—8, 11—12, 15—19 are pending.

The §103 Rejections

The Applicant submits that the Office has failed to establish a prima facie case of obviousness and, in view of the comments below, respectfully traverses the Office's rejections. However, before discussing the substance of the Office's rejections, a section entitled "The §103 Standard" is provided and will be used in addressing the Office's rejections. Following this section, a section entitled "The Vanstone Reference" is provided, which describes Vanstone's disclosure and teachings.

The §103 Standard

To establish a prima facie case of obviousness, three basic criteria must be met. MPEP § 2142. First, there must be some suggestion or motivation, either in the references themselves or in the knowledge generally available to one of ordinary skill in the art, to modify the reference or to combine reference teachings. In re Jones, 958 F.2d 347, 21 USPQ2d 1941 (Fed. Cir. 1992); In re Fine, 837 F.2d 1071, 5 USPQ2d 1596 (Fed. Cir. 1988). Second, there must be a reasonable

Response to Office Action Dated 07/25/2005

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expectation of success. In re Merck & Co., Inc., 800 F.2d 1091, 231 USPQ 375 (Fed. Cir. 1986). Finally, the prior art reference (or references when combined) must teach or suggest all the claim limitations. In re Royka, 490 F.2d 981, 180 USPQ 580 (CCPA 1974).

Hence, when patentability turns on the question of obviousness, the search for and analysis of the prior art includes evidence relevant to the finding of whether there is a teaching, motivation, or suggestion to select and combine or modify the references relied on as evidence of obviousness. The need for specificity pervades this authority. See, e.g., *In re Kotzab*, 217 F.3d 1365, 1371, 55 USPQ2d 1313, 1317 (Fed. Cir. 2000) ("particular findings must be made as to the reason the skilled artisan, with no knowledge of the claimed invention, would have selected these components for combination in the manner claimed").

The Vanstone Reference

In contrast to the Applicant's claims reciting "a memory device" and "a smart card" for interface to "a common computer". Vanstone discloses a system comprising only a terminal and a smart card. In particular, Vanstone discloses a method for verifying the authenticity of messages exchanged between a pair of correspondents (Vanstone, Abstract), such as a smart card and a terminal (column 2, lines 42—43). The verification is performed according to first and second signature algorithms (column 2, lines 43—53). Notably, a memory device—i.e. a device that is distinct from the smart card and the terminal (computer), as is the case in the Applicant's claims—is not present in the Vanstone system.

Vanstone teaches away from the Applicant's verification of a smart card and a memory device. In particular, Vanstone's method of verification requires—in contrast to the capabilities of memory devices—that processing power be

Response to Office Action Dated 07/25/2005

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24 25 available within each of the two devices within the verification system. particular, Vanstone discloses: use of an RSA algorithm for the card to verify the terminal (column 3, lines 17-25); and, use of an elliptic curve algorithm for the terminal to verify the card (column 3, lines 25-30). Thus, because both devices in the verification scheme must have the ability to process their ends of both algorithms, Vanstone teaches away from verification of a memory device that is not suited for such processing.

The Vanstone system and protocol, which is intended for a smart card and a terminal, is not adaptable to verification of a smart card and a memory device interfaced to a computer. In particular, the teachings of the Vanstone disclosure are operable only if each device (smart card and terminal) has some processing power to implement the protocol. The processing power is necessary to operate Vanstone's challenge-response protocol (column 3, lines 17—18) for authenticating a smart card and terminal. Thus, the Vanstone system and protocol is not adapted to verifying a smart card and a memory device, since the memory device would not be able to perform the challenge-response algorithm calculations.

The Vanstone method and/or protocol cannot be configured to enable and/or disable access to "a memory device" using "a smart card" interfaced with a "common computer". Vanstone discloses a terminal and a smart card, but does not disclose an additional memory device; i.e. Vanstone does not disclose a memory device that is attached to the computer along with the smart card. Thus, the Vanstone disclosure discloses no teachings that explain verification of such a memory device. Nothing in Vanstone, which presupposes that the devices to be

Response to Office Action Dated 07/25/2005

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24 25 verified have processing power, would suggest that Vanstone could be applied to such a system as recited by the Applicant's claims.

Traversal of the §103 Rejections

Claims 1, 3, 6—8, 12 and 16—17 are rejected under 35 U.S.C. §103(a) as being unpatentable over U.S. Patent No. 5,623,637, hereafter "Jones" in view of U.S. Patent No. 6,178,507, hereinafter "Vanstone." The Applicants respectfully traverse the rejection and request that the rejection be reconsidered and withdrawn.

Claim 1 recites a system for porting user data from one computer to another comprising:

- a memory device to store the user data;
- a smart card associated with a user that alternately enables access to the user data on the memory device when both the memory device and smart card are interfaced with a common computer and disables access to the user data when one of the memory device or smart card is absent; and
- wherein the memory device stores a public key and the smart card stores a corresponding private key and access to the user data in the memory device is enabled upon verification that the public key and the private key are associated.

The references of record fail to disclose a "memory device (that) stores a public key and the smart card stores a corresponding private key and access to the user data in the memory device is enabled upon verification that the public key and the private key are associated". The Vanstone reference was cited for this purpose; however, for the reasons seen herein, Vanstone fails to disclose a system and/or protocol that discloses the elements recited.

The Applicant observes that the Vanstone reference does not disclose storage of a public key in a memory device and storage of a corresponding private

Response to Office Action Dated 07/25/2005

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24 25 key in a smart card. Instead, Vanstone discloses a challenge/response protocol between a smart card and a terminal. No separate memory device is disclosed; accordingly, storage of the public key in a memory device (separate from the "smart card" and "common computer") is not shown.

The Vanstone system and protocol, which is intended for verification of a smart card and a terminal, is not adaptable to verification of a smart card and a The Applicant recites a smart card and a memory device, memory device. wherein both are connected to "a common computer". As seen in the above section entitled "The Vanstone Reference", which is incorporated herein by reference, the Vanstone protocol requires that both devices have at least some processing power. Because there is no reason to believe that a memory device could perform the algorithms disclosed by Vanstone, the teachings of Vanstone are not adapted to verifying the association of a smart card and a memory device, as recited by the Applicant's claims. Therefore, the Applicant's system is able to do what Vanstone's system is not able to do—i.e. verify keys without requiring a memory device to perform calculations that it cannot perform.

The Patent Office suggests that Vanstone's system and protocol can be adapted to verify a smart card and memory. However, the Patent Office appears to concede that, for the Vanstone system to operate, the memory device must be considered to be the terminal (see Office Action mailed 07/25/2005, page 4, line 2). This concession is necessary because the Vanstone disclosure requires that, for the two devices to be mutually verified, each must have sufficient processing power to perform according to the two challenge/response algorithms (see column 3, lines 17—25 and 25—30, and 16—17). Moreover, Vanstone docs not address

Response to Office Action Dated 07/25/2005

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any contingencies for adapting the algorithm to a case wherein one of the devices to be verified is a memory device.

The Applicant's claims recite a working system that does not make processing demands on the memory device. In contrast, the Vanstone reference cannot be adapted for operation between "a smart card" and "a memory device" "interfaced with a common computer", as recited by the Applicant's claim. This is because Vanstone's technology is configured to operate between a smart card and a terminal, where each device has sufficient processing power to perform the required algorithm steps. In contrast, the arrangement recited in the Applicant's claims is operable even though the memory device has no processing power. Therefore, incompatibilities between the Vanstone technology and elements recited in the claims render Vanstone's method ineffective within the context of the claims.

Therefore, because the Vanstone reference is not adaptable to the verification of a memory device and a smart card interfaced to a computer system, the Applicant respectfully requests that the rejection of claim 1 be removed.

Claim 7 is representative of independent claims 5, 11, 15, 17, 18 and 19, and recites a computer system, comprising:

- a computer having an interface; and
- a profile carrier adapted to use the interface, the profile carrier comprising a smart card associated with a user and a memory device having data memory to store a user's profile, wherein the smart card alternately enables access to the user's profile when present and disables access to the user's profile when absent;
- wherein the smart card stores a first key;
- wherein the data memory stores a second key that is associated with the first key;
- wherein the smart card is configured to authenticate the second key from the data memory using the first key as a condition for enabling access to the user data.

Response to Office Action Dated 07/25/2005

with respect to a memory device, wherein the smart card and memory device are interfaced to a computer. Vanstone instead discloses a protocol that authenticates a smart card with respect to a terminal. Vanstone requires that both of the two devices to be authenticated perform either the signature and/or verification protocol associated with the RSA and/or elliptic curve algorithm (see Vanstone, column 3, lines 10—36). Thus, Vanstone discloses a method to authenticate devices that are configured to perform such computations. Moreover, Vanstone's teachings do not suggest any means for adaptation to a system, as recited, wherein one of the devices is a memory device.

As seen in the discussion of Claim 1, the Vanstone reference does not

The Patent Office repeats the rejection as stated with respect to claim 1. Accordingly, the Applicant incorporates the arguments above herein. As a result, claims 5, 11, 15, 17, 18 and 19 are allowable for substantially the same reasons that claim 1 is allowable, as seen above, as well as for reasons associated with the elements recited by each claim individually.

Claims 3, 4, 6, 8, 12 and 16 depend from the above-mentioned independent claims and are allowable as depending from an allowable base claim. These claims are also allowable for their own recited features that, in combination with those recited in the corresponding base claim, are neither disclosed nor suggested in references of record, either singly or in combination with one another.

Conclusion

Claims 1 and 3—8, 11—12, 15—19 are in believed to be in condition for allowance. Applicant respectfully requests reconsideration and prompt issuance of

Response to Office Action Dated 07/25/2005

the present application. Should any issue remain that prevents immediate issuance of the application, the Examiner is encouraged to contact the undersigned attorney 2 to discuss the unresolved issue. Respectfully Submitted, 5 Lee & Hayes, PLLC 6 421 W. Riverside Avenue, Suite 500 Spokane WA 99201 8 11-16-2005 By: Dated: David S. Thompson 9 Reg. No. 37,954 Attorney for Applicant 10 11 LEE & HAYES PLLC Suite 500 12 421 W. Riverside Avenue 13 Spokane, Washington 99201 Telephone: 509-324-9256 x235 14 Facsimile: (509) 323-8979 15 16 17 18 19 20 21 22 23 24

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